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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

August 1, 2003, pp. 12-18.

ATTY. DOCKET NO.	APPLN. NO.		
174/304	10/723,530		
APPLICANT David Lewis	CONF. NO. Not yet assigned		
FILING DATE	GROUP ART UNIT		
November 24, 2003	Not yet assigned		

TO ADDUMENT U.S. PATENT DOCUMENTS **FILING DATE** DOCUMENT **EXAMINER** DATE NAME **CLASS SUBCLASS** IF INITIAL NUMBER APPROPRIATE 11/02/93 Cliff et al. 307 465 5,258,668 784 12/28/93 Cliff et al. 364 5,274,581 Hsieh et al. 364 784 03/15/94 5,295,090 307 465 09/20/94 New 5,349,250 10/25/94 Veenstra 307 465 5,359,242 Goetting et al. 326 39 11/15/94 5,365,125 01/02/96 New et al. 326 38 5,481,206 Cliff et al. 364 716 01/02/96 5,481,488 5,483,478 01/09/96 Chiang 364 787 Goetting et al. 326 39: 03/19/96 5,500,608 786 06/04/96 Hsieh et al. 364 5,523,963 08/13/96 New et al. 326 38 5,546,018 364 787 05/13/97 New 5,629,886 326 39 05/20/97 Lee et al. 5,631,576 41 326 5,672,985 09/30/97 Lee 326 41 5,675,262 10/07/97 Duong et al. 5,724,276 03/03/98 Rose et al. 364 716.08 FOREIGN PATENT DOCUMENTS **TRANSLATION** EXAMINER **SUBCLASS DOCUMENT NUMBER** DATE COUNTRY **CLASS** INITIAL YES NO OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.) **EXAMINER** INITIAL Altera Corporation, "Stratix Device Handbook, Volume 1," July 2003, pp. 1-1 - 1-2, 2-3 - 2-14. 41 Xilinx, Inc., "Virtex-II Platform FPGAs: Introduction and Overview," Product Specification, Version 2.0, August 1, 2003, p. 1. Xilinx, Inc., "Virtex-II Platform FPGAs: Detailed Descripton," Product Specification, Version 3.0,

EXAMINER

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE B. 1 9 2004 H STATEMENT BY APPLICANT			/APPLICANT David Lewis FILING DATE		CONF. NO. Not yet assigned GROUP ART UNIT		
ă			November 24, 2003		Not yet assigned		
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						YES	NO
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